

PRESS RELEASE

31ST INTERNATIONAL CONFERENCE ON VLSI DESIGN AND THE 17TH INTERNATIONAL CONFERENCE ON EMBEDDED SYSTEMS RETURNS TO PUNE IN JANUARY 2018

Industry and Academic Luminaries Expected to Extensively Discuss the Design and Use of Safe, Secure and Intelligent Systems in Various Applications such as Robotics, Self-Driving Cars, Automation and Artificial Intelligence.

Pune, India – October 7, 2017: The organizing committee of the 31st International Conference on VLSI Design (“VLSID”) and the 17th International Conference on Embedded Systems today announced that the VLSI Society of India (“VSI”) has selected Pune to host the conference for the second time. The conference will be held at the Hyatt Regency hotel from January 6th, 2018 to January 10th, 2018.

Conference registrations are now open. Please click on this [link](#) to register.

“The conference coming back to Pune is a testament to the rise and increasing importance of Pune as a high technology hub,” said Jaswinder Ahuja, President of VSI and Managing Director of Cadence Design Systems (India) Pvt. Ltd. “We expect that strong participation from local academic institutions, OEMs and embedded systems and semiconductor companies will make the conference successful.”

“We are extremely pleased to host India’s most premier technical conference in Pune the second time around. Moreover, the theme of safe and secure intelligent systems is the most pressing opportunity and challenge before us” said Niranjan Pol, the conference’s General Chair and Member of the Executive Council of IESA (India Electronics & Semiconductor Association). “Technologies such as artificial intelligence, robotics, automation, smart cities and self-driving cars are going to have a cross-generational disruptive impact on the way we live, the way we work and the way we enjoy life.”

Conference Program

The technical program at the conference is expected to cover a broad range of topics relevant to electronic and embedded system design and foster discussions between top industry and academic experts, and attendees. The original high-quality peer-reviewed papers presented at the conference will be published on IEEE Xplore.

- 8-Jan-2018: Inauguration, Technical Sessions, Tracks (Design Contest, Ph.D. Forum, User/designer Track, Industry Forum, Women in Engineering), Exhibits
- 9-Jan-2018: Technical Sessions, Tracks (Design Contest, Ph.D. Forum, User/designer Track, Industry Forum, Women in Engineering), Exhibits, Banquet Dinner
- 10-Jan-2018: Technical Sessions, Tracks (Design Contest, Ph.D. Forum, User/designer Track, Industry Forum), Exhibits
- 6-Jan-2018: Full and half day tutorials
- 7-Jan-2018: Full and half day tutorials

Key Sponsors



About Exhibits Area

As part of this conference, sponsors and exhibitors will have an opportunity to showcase their products demonstrations, achievements, organization capabilities to the potential customers and to the academia in an exhibit space of ~10,000 Sq. feet. In addition, a separate exhibit area for start-up companies to showcase their products will be provided at a nominal cost.

About Startup Forum

This conference will provide a platform for the budding startup community in and around Pune to showcase their ideas and product successes through booth demos and tracks.

About Student Conference

The conference is proud to continue the tradition of organizing a student conference. This year, around eight hundred students are expected to benefit immensely from this opportunity to meet industry experts in the area of VLSI / Embedded Systems. Exposing students, our future work force, to cutting edge technologies and research at this stage in their career will go a long way in supporting the Prime Minister's dream initiative of 'Make in India'.

About Tutorial Program

The conference features a two-day dedicated world class tutorial program, with speakers from renowned universities all over the world, including Stanford, University of Texas, Penn State, Georgia Tech, IIT Madras, University of Wisconsin, Madison, University of Florida, Purdue, Colorado State and IIT

Kharagpur. The tutorial topics span from Analog/Mixed-signal IC design to emerging FinFET transistor technology to Embedded Systems applications such as "Assistive Technology for Visually Impaired", Safe Autonomous Systems and IoT security.

About VLSID

This joint conference is a forum for researchers and designers to present and discuss current topics in VLSI design, electronic design automation, embedded systems, and enabling technologies. Two days of tutorials will be followed by three days of regular paper sessions, special sessions, and embedded tutorials. Industry presentation sessions along with exhibits, panel discussions, Design Contest, and Education Forum, round off the program.

The theme for the conference is Safe and Secure Intelligent Systems, that explores the ability of VLSI and embedded circuits and systems to positively impact several application areas – robotics, industrial control, automation, artificial intelligence, smart cities and smart transportation. Example areas under the theme include (but are not restricted to) designing self-learning computational systems, improving the efficiency and accuracy of financial and medical systems, enabling robots to learn from experience and improve productivity, developing intelligent monitoring and control systems for smart grids, smart cities and smart transportation, and using integrated circuits and embedded systems to leverage novel algorithms and technologies.

About Pune

Called the Queen of the Deccan, and the cultural capital of Maharashtra, Pune is a city with a future that promises to be as interesting as its history. According to a recent opinion poll conducted for The Times of India by leading market research firm IMRB across the country's eight largest urban cities, Pune ranked as the second best place to live in. Surrounded by verdant hills and beautiful lakes, the city has a temperate climate throughout the year and is among the greenest urban areas in the country. It is the center of traditional Marathi culture, in which education, arts and theatre are given pride of place. Pune has one of India's oldest universities and its numerous colleges attract both Indian and international students, which is probably why it is called the Oxford of the East.

Pune is fast emerging as a prominent location for semiconductor, embedded systems, IT and manufacturing companies to expand in. The city has the eighth largest metropolitan economy and the sixth highest per capita income in the country. Some of the most reputed VLSI and embedded systems leaders have design centers located in Pune. These include Seagate, John Deere, Eaton Corp, Marvell Semiconductors, Nvidia and Cadence. Pune is the largest automotive hub in India with companies like Tata Motors, General Motors, Mercedes Benz, Audi, Bajaj Auto, Force Motors, and Volkswagen having their design and manufacturing operations located in Pune.

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