

Call for Contribution

The 34th International Conference on VLSI Design (VLSID-2021) is the main event in India in the field of VLSI design, bringing together all the stakeholders that includes academia, industry, R&D houses and policy makers in the field of hardware and software system design, verification, test, EDA tools development, and manufacturing of electronic circuits. It is a flagship event of VLSI Society of India. The 20th International Conference on Embedded Systems is collocated with VLSID-2021. This edition is jointly organized by IIT Guwahati and IIT Roorkee, and is being hosted at IIT Guwahati from February 20 – 24, 2021. The six-day event consists of a conference with keynotes, plenary sessions, regular papers, tutorials, workshops, design contests, industry and Ph.D. forums. The conference is complemented by an exhibition showing the state-of-the-art in system design, software tools and techniques. The conference creates an excellent platform for the delegates to interact with all stakeholders in the electronic system design and automation domain. The theme for VLSID-2021 **“From the transistor to cyber-physical systems for solving societal challenges”** aims to emphasize the potential of the VLSI community to find multidisciplinary solutions for the societal and engineering challenges of our times.

Keeping in line with the conference theme, this year’s event will highlight the following focus areas.

- **Emerging Technologies**
- **Smart Systems for a Smarter World**
- **Innovation in Neuro Technologies**
- **Big Data Processing**
- **Sustainable Computing and Systems**
- **Energy-aware Systems**

The conference invites original unpublished contributions in the following tracks.

1. **Microarchitecture Design**
2. **Digital Integrated Circuits and Systems**
3. **Biomedical Circuits and Healthcare Systems**
4. **Electronic Design Automation**
5. **Security and Privacy**
6. **Test, Verification and Reliability**
7. **Emerging Technologies and new age Nano electronics**
8. **Analog and Mixed-Signal Design/RF Circuit Design**
9. **Embedded System Design and IoT**
10. **Artificial Intelligence and Machine Learning**
11. **Miscellaneous (Topics within the conference theme but not fitting to the above tracks)**



VLSI Design 2021- 34th International Conference on VLSI Design and 2021 20th International Conference on Embedded Systems

FEB 20-24, 2021

Important Dates

Submission deadline for special session proposals#	: 30 October 2020
Notification for acceptance for special session proposals	: 30 November 2020
Submission deadline for tutorial proposals	: 15 November 2020
Notification of acceptance for tutorials	: 30 December 2020
Full paper submission deadline for regular papers	: 30 September 2020
Notification for acceptance for regular papers	: 15 November 2020
Submission deadline for camera-ready paper	: 15 December 2020
Submission deadline for Student Research Forum (SRF)	: 15 November 2020
Submission deadline for Industry Forum (IF)	: 15 November 2020
Acceptance notification for SRF/IF	: 30 December 2020

Send proposals to vsid2021@gmail.com

VLSID-2021 Committee

Vishwani D. Agarwal , Auburn University	Steering Committee Chair
Satya Gupta , VSI President	Advisor
Sudeb Dasgupta , IIT Roorkee	General Co-Chair
Gaurav Trivedi , IIT Guwahati	General Co-Chair
Vijay Raghunathan , Purdue University	Program Chair
V. Kamakoti , IIT Madras	Program Co-Chair
Saibal Mukhopadhyay , Georgia Tech	Program Co-Chair
Sri Parameswaran , University of New South Wales	Tutorial Chair
Anand Bulusu , IIT Roorkee	Tutorial Co-Chair
V. Janakiraman , IIT Madras	Tutorial Co-Chair
Preet Yadav , Wipro Ltd.	Organizing Co-Chair
Devesh Dwivedi , Samsung Semiconductor India R&D	Organizing Co-Chair
Siddarth Gupta (ARM)	Industry Forum Co-Chairs
Ayan Dutta (Intel)	Industry Forum Co-Chairs
Aryabartta Sahu , IIT Guwahati	Publication Co-Chair
Karun Rawat , IIT Roorkee	Publication Co-Chair
Chandan Karfa , IIT Guwahati	Student Research Forum Co-Chair
B. P. Das , IIT Roorkee	Student Research Forum Co-Chair
Sanjeev Manhas , IIT Roorkee	Sponsorship Chair
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Aryabartta Sahu , IIT Guwahati	Publication Co-Chair
Nagi Naganathan , Northrop Grumman	IEEE/ACM Liaison
Chitra Hariharan , Intel India	VSI Liaison

Webpage: <http://embeddedandvlsidesignconference.org/>



VLSI DESIGN 2021
FEB 20 - FEB 24, 2021
(HOSTED BY VLSI SOCIETY OF
INDIA IN ASSOCIATION WITH IIT
GUWAHATI AND IIT ROORKEE)