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Everyday Genius: How Design Makes Technology Better

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More than half of the world's population now has a mobile subscription, as driven by smarter mobile devices going beyond information technology and becoming an integral part of people's lives. Consumers' demand for trended slim designs, bigger screens and diverse applications is then driving mobiles to be increasingly powerful with extremely superior power efficiency. Confronting the following design challenge under the mission to enhance and enrich everyone's lives, MediaTek Director Shivananda (Shivoo) Koteshwar will share in this talk the implementation of next generation mobile processor made possible with advanced technologies, design innovation and key partnerships with vendors and customers.

Biography: Shivananda (SHIVOO) R Koteshwar is responsible for Physical Implementation and Design verification technologies in design technology development and implementation for all of MediaTek's product lines including wireless communications, mobile application processors, wireless connectivity, home entertainment, automotive electronics and broadband/of networking business.

Shivoo has over 19 years of experience in the semiconductor industry in varying positions in Synopsys, Intel and Arcus Technologies; He has in-depth knowledge of ASIC Design Flow/Methodologies, Product Management, Technical Implementation, Customer Management, Organization Design and Operations Management in both private funded & large public companies in the semiconductor industry. Shivanada is a versatile technology leader with career long record of promotion, stakeholder satisfaction, team building and strategic insight.

Shivoo took a break from the semiconductor industry for 2 years in 2012 and set up the India's first college preparatory co-educational residential K12 School, "The Amaatra Academy". Shivanada was the Founding Director of the school and also worked as a full-time Professor in E&C Department in PES Institute of Technology (PESIT).

Shivoo is an alumnus of Indian Institute of Management, Bangalore, has a Masters Degree in Electrical Engineering from Oregon Graduate Institute, USA and BTech in E&C from Mysore University. He is a 2012 Fellow in the Start-up Leadership Program for Bangalore Chapter. He is currently a Research Scholar, pursuing dual PhD in VLSI area at Visvesvaraya Technological University and in General Management at Bangalore University .