

VLSID 2013 CALL FOR PAPERS



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VLSID 2013 – The 26th International Conference on VLSI Design: concurrently with the 12th International Conference on Embedded Systems Design taking place in **Jan 5-10 in Pune, India** – is a forum for researchers/designers to present/discuss current topics in VLSI design, electronic design automation, embedded systems, etc. The Theme of the Conference is **Green Technology**

is a forum for researchers and designers to present and discuss current topics in VLSI design, electronic design automation, embedded systems, and enabling technologies. Two days of tutorials will be followed by three days of regular paper sessions, special sessions, and embedded tutorials. Industry presentation sessions along with exhibits, panel discussions, Design Contest, and Education Forum round off the program.

The theme for the conference is Green Technology - A New Era for Electronics, which explores the ability of VLSI and embedded circuits and systems to positively impact the environment. Example areas under the theme include (but are not restricted to) designing energy-efficient VLSI circuits, improving the efficiency of energy-hungry applications such as data centers, developing intelligent monitoring and control systems such as smart grids, and using integrated circuits or embedded systems to leverage novel green technologies.

Pune city, also known as the Oxford of the East, has witnessed huge growth in its VLSI and embedded community over the past few years and is proud to host its very first VLSI Design Conference. The conference organizing committee is looking forward to making this an unforgettable experience for all attendees.

Topics of interest

Papers are invited on topics related to (but not limited to) the following areas. Accepted and presented papers will be published in the VLSID 2013 Conference Proceedings and IEEEExplore®.

VLSI Design and Methodologies: Analog /RF mixed signal design, Synthesis/DFT, Verification, MEMS, nanoscale computing and nano-electronics, architectures etc.

Embedded Systems: Audio, Image and Video processing, Hardware/Software co-design and verification, reconfigurable systems, applications in DSP, communications, encryption, security, compression, hybrid systems-on-chip, embedded software tools, case studies, ESL design flows.

Theme based – Green technology: Energy-efficient design, ultra-low-power, cross layer power etc.

Submission Categories

Regular Papers: Please submit previously unpublished papers electronically at the conference website by July 24th, 2012. The manuscript should clearly state the novel ideas, results and applications of the contribution. Submissions will undergo a DOUBLE BLIND REVIEW.

Half-day and Full-day Tutorials: We invite proposals for hands-on tutorials that explore an advanced technology in the area of VLSI or Embedded Systems. Proposals for full or half-day tutorials must be submitted electronically at the conference website by August 2nd, 2012.

Embedded Tutorials, Special Sessions, Panels: Proposals for embedded tutorials (short tutorials) and special sessions (a set of related papers on a subject) in emerging areas will be considered for inclusion in the technical program. For panels, proposals must be submitted with an abstract, together with names of panelists.

Design Contest: We solicit entries for the design contest from full-time graduate and undergraduate students, and professionals from industry and research establishments. Entries must be submitted electronically at the conference website by Sept 15th, 2012.

User/Designer Track: We are soliciting proposals for presentations or posters for the User/Designer track. Proposals should be submitted electronically at the conference website by August 2nd, 2012. Complete submission details are available at the website www.vlsidesignconference.org.

RASDAT: An international workshop on reliability aware system design and test will be held on Jan 9th - 10th in conjunction with the Conference.

MS Word and Latex templates are available at http://www.ieee.org/conferences_events/conferences/publishing/templates.html

Exhibits: Please contact the exhibits chair to explore opportunities for companies and institutions to display their products/services.

Fellowships: The conference will award fellowships, based on need and merit, to partially cover expenses of attendees from India. Candidates with accepted submissions will be given preference. Application details will be posted at the website.

Submissions (Paper) : July 24, 2012

Submissions (Tutorial/Special Session/User designer Track) : August 2, 2012

Acceptance of notification : September 7, 2012

Design Contest Submissions : September 15, 2012

Camera ready paper due : October 1, 2012

VLSID 2013 Conference : Jan 5 - 10, 2013

RASDAT Workshop : Jan 9 - 10, 2013

IMPORTANT DATES

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